

Electronic Acknowledgement Receipt

EFS ID:	5707907
Application Number:	10593674
International Application Number:	
Confirmation Number:	2861
Title of Invention:	Chip Resistor and Manufacturing Method Thereof
First Named Inventor/Applicant Name:	Masanori Tanimura
Customer Number:	52835
Filer:	Douglas P. Mueller/Heidi McCarty
Filer Authorized By:	Douglas P. Mueller
Attorney Docket Number:	10921.0427USWO
Receipt Date:	15-JUL-2009
Filing Date:	21-SEP-2006
Time Stamp:	16:12:15
Application Type:	U.S. National Stage under 35 USC 371

Payment information:

Submitted with Payment	no
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1	Miscellaneous Incoming Letter	transmittal.pdf	20602 d1809529ba6c6cd7180a26d793da7931e61ae19468d	no	1

Warnings:

Information:

2	Transmittal Letter	IDS.pdf	111953	no	4
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Warnings:					
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3	NPL Documents	JPofficeaction.pdf	63116	no	2
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Warnings:					
Information:					
4	Foreign Reference	JP2002194601.pdf	315576	no	6
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Warnings:					
Information:					
5	Foreign Reference	JP2004153160.pdf	484897	no	12
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6	Foreign Reference	WO9918584.pdf	2132000	no	88
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Warnings:					
Information:					
7	Foreign Reference	JP729704.pdf	572322	no	12
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If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

New International Application Filed with the USPTO as a Receiving Office

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